



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application Serial No. .... 09/388,857  
Filing Date ..... September 1, 1999  
Inventor ..... Luan C. Tran  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2813  
Examiner ..... L.M. Schillinger  
Attorney's Docket No. .... MI22-878  
Customer No. .... 021567

Title: Semiconductor Processing Methods Of Forming Transistors, Semiconductor Processing Methods Of Forming Dynamic Random Access Memory Circuitry, And Related Integrated Circuitry

**RESPONSE TO MARCH 23, 2004 FINAL OFFICE ACTION**  
**ACCOMPANYING REQUEST FOR CONTINUED EXAMINATION (RCE)**

To: Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

From: D. Brent Kenady  
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Responsive to the Final Office Action dated March 3, 2004, Applicant amends and remarks as follows:

**AMENDMENTS**

Underlines indicate insertions and ~~strikeouts~~ indicate deletions.